

2827

#5  
3/14/02  
Dra

THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Donald Craig. Foster  
Assignee: Amkor Technology, Inc.  
Title: Semiconductor Package And Leadframe With Horizontal Leads  
Spaced In The Vertical Direction And Method Of Making  
Serial No.: 09/932,290 Filing Date: August 17, 2001  
Examiner: Not Yet Assigned Group Art Unit: Not Yet Assigned  
Docket No.: M-11313 US

San Jose, California  
February 7, 2002

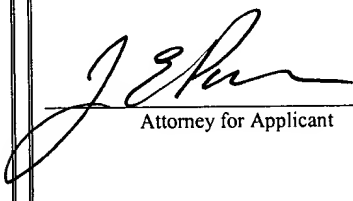
Attn: Official Draftsperson  
COMMISSIONER FOR PATENTS  
Washington, D. C. 20231

**SUBMISSION OF FORMAL DRAWINGS**

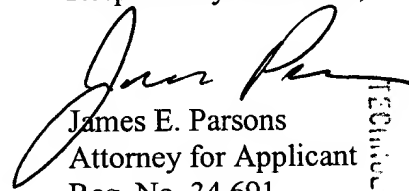
Dear Sir:

Applicant submits 11 sheets of formal drawings, consisting of Figures 1, 2A, 2B, 2C, 2D, 3, 4A, 4B, 4C, 5, 6A, 6B, 6C, 7A, 7B, 7C, 8, 9, 10, 11, and 12, in the above-named application. If there are any questions regarding these drawings, please call the undersigned at (408) 453-9200.

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, Washington, D.C. 20231, on February 7, 2002.

  
Attorney for Applicant  
2/7/02  
Date of Signature

Respectfully submitted,

  
James E. Parsons  
Attorney for Applicant  
Reg. No. 34,691

TECHNOLOGY CENTER 2800

REC-1 2002

RECEIVED

LAW OFFICES OF  
SKJERVEN MORRILL  
MacPHERSON LLP  
25 METRO DRIVE  
SUITE 700  
SAN JOSE, CA 95110  
(408) 453-9200  
FAX (408) 453-7979